



Combined Form PTO/SB/08A&B  INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
Sheet	1	of	1	Application Number	10/577,490
				Confirmation Number	5105
				Filing Date	April 27, 2006
				First Named Inventor	Shoji KAWAHITO et al.
				Art Unit	2819
				Examiner Name	Peguy Jean Pierre
				Attorney Docket Number	062487

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
	4	STEVEN DECKER et al.: "A 256 CMOS Imaging Array with Wide Dynamic Range Pixels and Column-Parallel Digital Output" IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE SERVICE CENTER, PISCATAWAY, NJ, US, vol. 33, no. 12, December 1998.	
	5	GARRITY D et al.: "A 10 bit, 2Ms/s, 15 mW BiCMOS cyclic RSD A/D converter" BIPOLAR/BICMOS CIRCUITS AND TECHNOLOGY MEETING, 1996., PROCEEDING OF THE 1996 MINNEAPOLIS, MN, USA 29 SEPT -1 OCT 1, NEW YORK, NY, USA, IEEE, US, 29 September 1996, PAGES 192-195.996	
	6	Supplementary European Search Report dated September 25, 2006, issued in corresponding European Patent Application Number 04 79 3319.7.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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